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**RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2816**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 2125

ISODA

Art Unit: 2816

Serial No.: 09/811,456

Examiner: L. Nguyen

Filed: March 20, 2001

Docket No.: 108075-00056

For: IMPROVED INPUT BUFFER CIRCUIT FOR SEMICONDUCTOR DEVICE

AMENDMENT UNDER 37 C.F.R. § 1.121

Introductory Comments

Director of the U.S. PTO
P.O. Box 1450
Alexandria, Virginia 22313-1450

July 22, 2004

Sir:

In response to the Office Action dated March 22, 2004, the period for response being extended from June 22, 2004 to July 22, 2004 by the attached Petition for Extension of Time, please amend the above-titled application as follows: